ABOUT THE INSTITUTION

AAA college of Engineering & Technology, (AAACET) Sivakasi is one among the renowned colleges for technical education in the state of Tamilnadu. AAACET is NAAC accredited with A grade and accredited by NBA (CIVIL, CSE, ECE, EEE and MECH) and is branded for its quality education, promoted by Sonny Vinayaga group of industries. Aspire, Attain, Achieve are its motto. The primary aim of our college is to provide quality education. The college is managed by Panjurajan- Amaravathy trust located at Thiruthangal, Virudhunagar District. Our college has received the Best Infrastructure Award from Dr. K. Rosaiah, the former Governor of Tamilnadu. The college has signed MoU's with leading industries and has established Centre of excellences in Industrial Internet of Things (IIoT), BOSCH – Centre for car, e-Yantra Robotics Lab, Amazon Web services, Oracle Workforce Development and Texas Instruments Innovation Lab

ABOUT THE DEPARTMENT

Department of Electronics and Communication Engineering was established in the year 2013 with on aim of creating quality electronics and communication engineers with sound technical knowledge in emerging technologies . The Department boasts a team of highly experienced and accomplished faculty members. It has consistently achieved excellent results in the university examination and have obtained Centre of Excellence in "Industrial Internet of things(IIoT)"' e-Yantra Robotics Laboratory and Texas Instrument Innovation Lab. The department has well established IETE Students forum through which various technical activities are conducted every year. Department has state of the art Laboratories such as Advanced communication lab, Microprocessor lab, Electronics lab, Optical and Microwave lab, Digital/Analog circuit lab and VLSI lab. The department has MOU with BSNL & various other industries, to provide real time training for the students in the field of advanced communications. Students are given exposure to industries by periodically arranging guest lectures by industrial experts and industrial visits.

ORGANIZING COMMITTEE

CHIEF PATRONS

Dr. P. Karvannan, Secretary

Dr. P. Ganesan, Correspondent

Dr. K. Vignesh Kumar, Joint Secretary

PATRON

Dr. M. Sekar, M.E., Ph.D., FIE., Principal,

CONVENOR

Dr.T. Senthil, M.E, Ph.D, Professor & HOD/ECE, AAACET.

COORDINATORS

Mrs. K. Padmapriya, M.E.,(Ph.D)/AP,ECE

Mrs. M. Gayathri, M.E., /AP,ECE

ADDRESS FOR COMMUNICATION

Mrs. K. Padmapriya/AP,ECE
AAA college of Engineering and Technology, Amathur, Sivakasi-626005
Email:padmapriya@aaacet.ac.in
phone: 8531822334

VENUE :THOMAS ALVA EDISON HALL,CAMBRIDGE BLCK.

AAACOLLEGE OF ENGINEERING AND TECHNOLOGY

Five Days Faculty Development Programme on

"AI Driven Semiconductor Device Design and Modelling for Next Generation Computing Systems"

03.02.2025 to 07.02.2025

Organized by

Department of Electronics and Communication Engineering
in association with

IETE Student Chapter

Website: https://aaaenggcoll.ac.in/

RESOURCE PERSONS

Dr. Suresh Balanethiram,
Assistant Professor, NIT, Puducherry.

Mr.V. Navaneetha Krishnan,
Senior Field Application Engineering, Entuple Technologies Pvt. Ltd.
Bengaluru, Karnataka.

Dr.P. Sivakumar,
Dean SEET, Professor, Department of ECE, Kalasalingam Academy of Research and Education, Krishnankovil.

Dr. D. Gracia Nirmala Rani,
Associate Professor, Thiagarajar College of Engineering, Madurai.

Dr. G. Dhanabalan,
Associate Professor / ECE,
Vel Tech Rangarajan Dr. Sagunthala R&D Institute of Science and Technology, Chennai.

IMPORTANT DATES

Starting date for Registration - 09.1.2025
Last date for Registration   - 31.1.2025
Intimation of Selection   - 01.2.2025

REGISTRATION FEE

- Registration fee: Rs. 500/-
- Registration fee may be paid through Gpay.
- As the numbers of seats are limited registration will be confirmed on first come first serve basis.
- Certificates will be provided to all the participants.

PAY NOW

UPI ID: gayathrimuthumanickam@okobi

E-mail the scanned copies of filled-in and duly signed application form along with Payment screenshot to

padmapriya@aaacet.ac.in

ABOUT THE FDP

The Faculty Development Program (FDP) on "AI-driven Semiconductor Device Design and Modelling for Next Generation Computing Systems" aims to equip participants with the knowledge and skills required to leverage artificial intelligence (AI) and machine learning (ML) techniques in designing and modeling semiconductor devices. The program covers the fundamentals of AI-driven design and modeling, machine learning algorithms for semiconductor device design, and neural network-based modeling of semiconductor devices. Participants will also learn about AI-driven optimization of semiconductor device performance and explore case studies on AI-driven design of emerging devices such as FinFETs and nanowire FETs for next generation computing system. Through hands-on sessions, participants will work on AI-driven design and modeling projects using popular software tools.

TARGET AUDIENCE

Faculty members of AICTE approved instituions, Universities, Ploytechnic Colleges, Research scholars, Engineers from R&D Labs and Government Sectors.

TOPICS COVERED

- Introduction to Semiconductor Design, ML for VLSI Design
- Integrating AI in VLSI Design and Semiconductor process Automation
- Design Challenges and Opportunities - Emerging Memory Technologies and their role with ML
- Hardware Fundamentals for Machine Learning
- Hardware Accelerator for Data Flow Systems
- Cutting-Edge Custom IC Design: A Guide to Cadence EDA Tools
- Machine Learning for Physical Design Automation
- Machine Learning for Verification and Validation

REGISTER NOW

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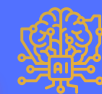**in association with**
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Five days Faculty Development Programme On

"AI driven Semiconductor Device design and modelling for Next Generation Computing Systems"

03.02.2025 to 07.02.2025

REGISTRATION FORM

Name : ....

Designation : ....

Organization : ....

Address : ....
....

E-Mail : ....

Mobile No : ....

Accommodation Required: YES / NO

Payment Details : ....

Transaction Number : ....

Date   :   ..../........../2025

Signature of the
HoD / Head of the Institution

Signature of the
Applicant

Website: https://aaaenggcoll.ac.in/